

FIG. 1

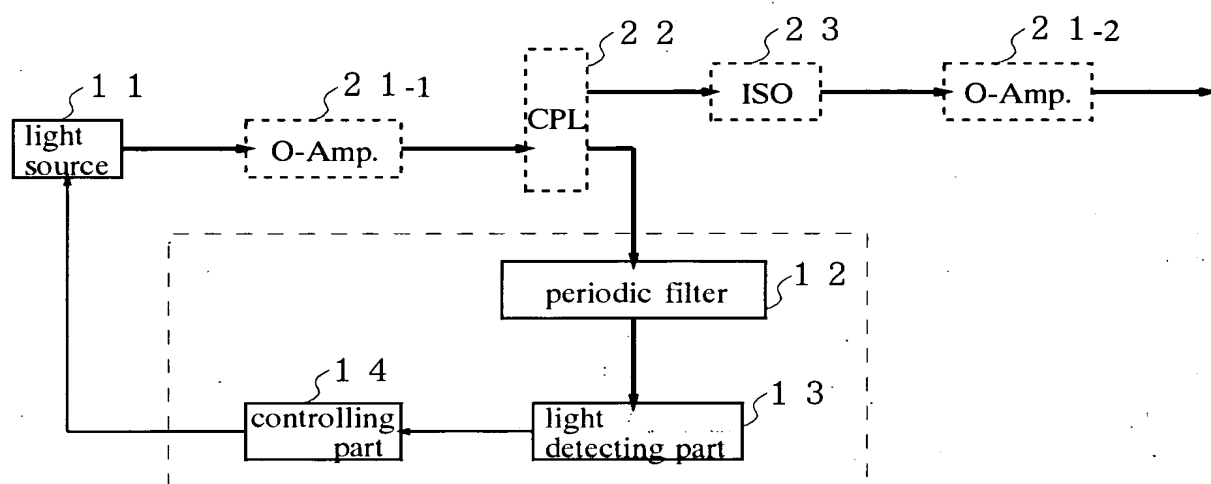
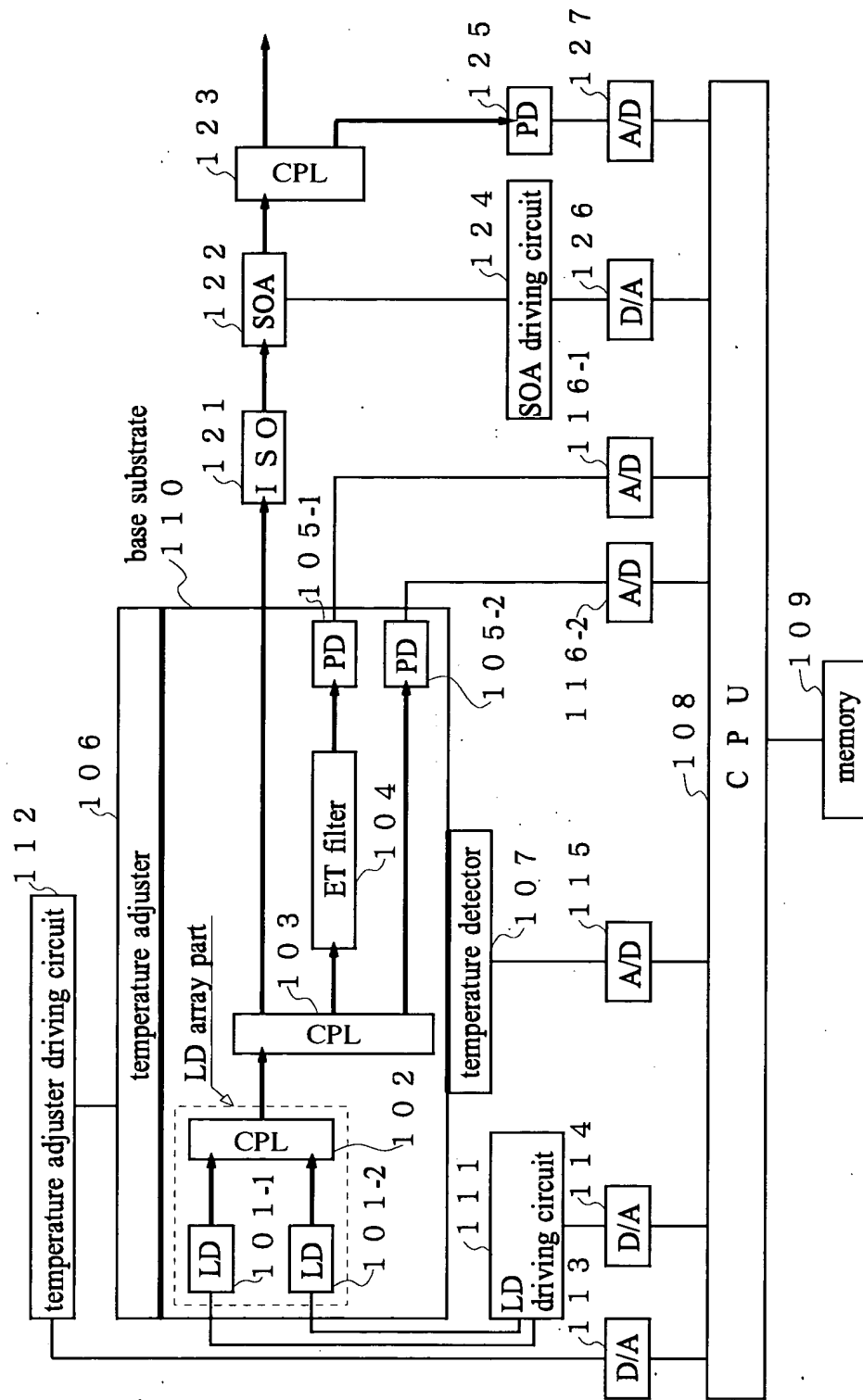


FIG. 2



F I G . 3

device temperature (°C)	channel	grid		LD
		(n m)	(T H z)	
1 6	c h 0	1 5 3 5 . 8 2	1 9 5 . 2 0	LD101-1
2 4	c h 1	1 5 3 6 . 6 1	1 9 5 . 1 0	
3 2	c h 2	1 5 3 7 . 4 0	1 9 5 . 0 0	
4 0	c h 4	1 5 3 8 . 1 9	1 9 4 . 9 0	
1 6	c h 5	1 5 3 8 . 9 8	1 9 4 . 8 0	LD101-2
2 4	c h 6	1 5 3 9 . 7 7	1 9 4 . 7 0	
3 2	c h 7	1 5 4 0 . 5 6	1 9 4 . 6 0	
4 0	c h 8	1 5 4 1 . 3 5	1 9 4 . 5 0	

FIG. 4

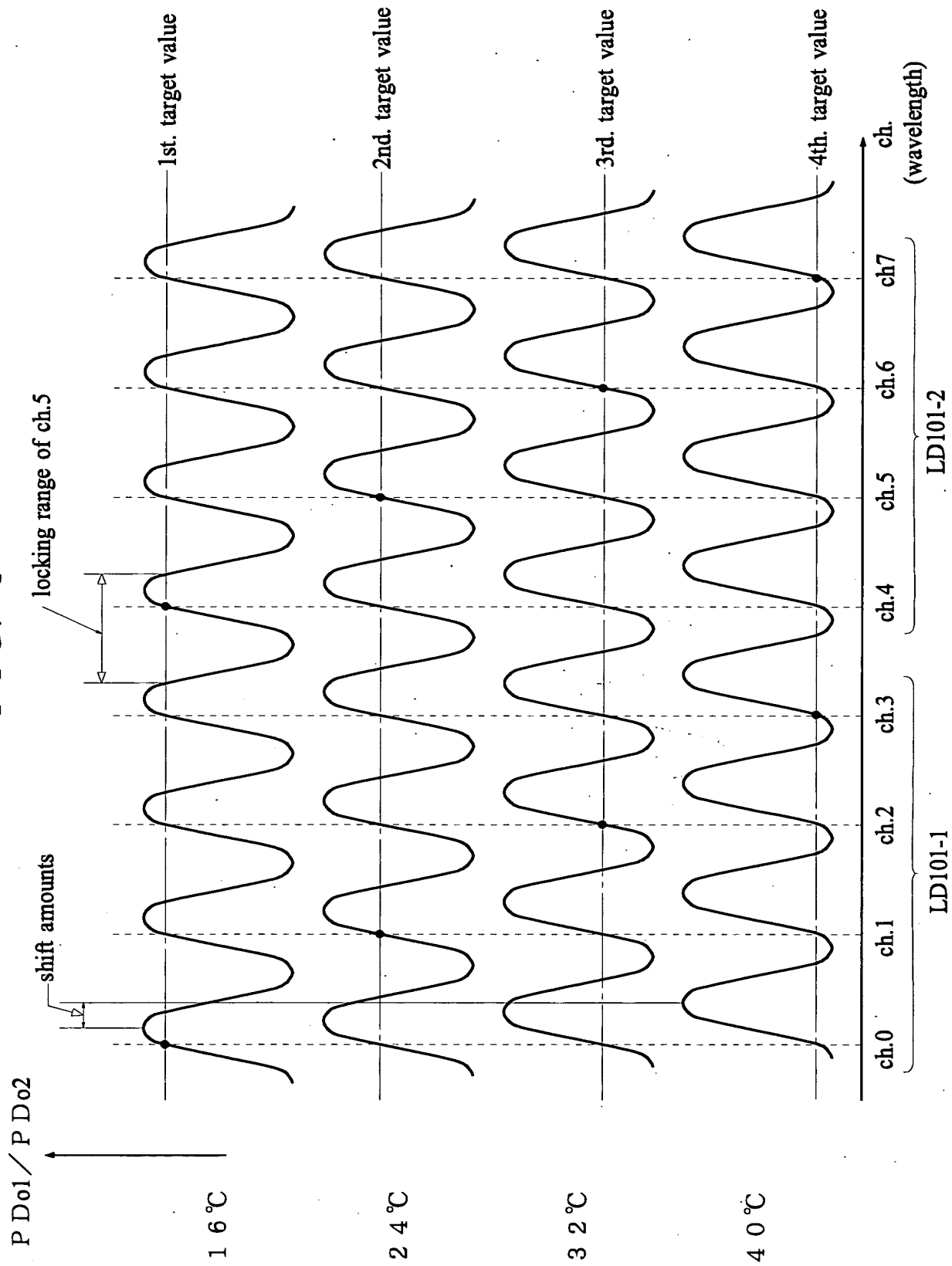


FIG. 5

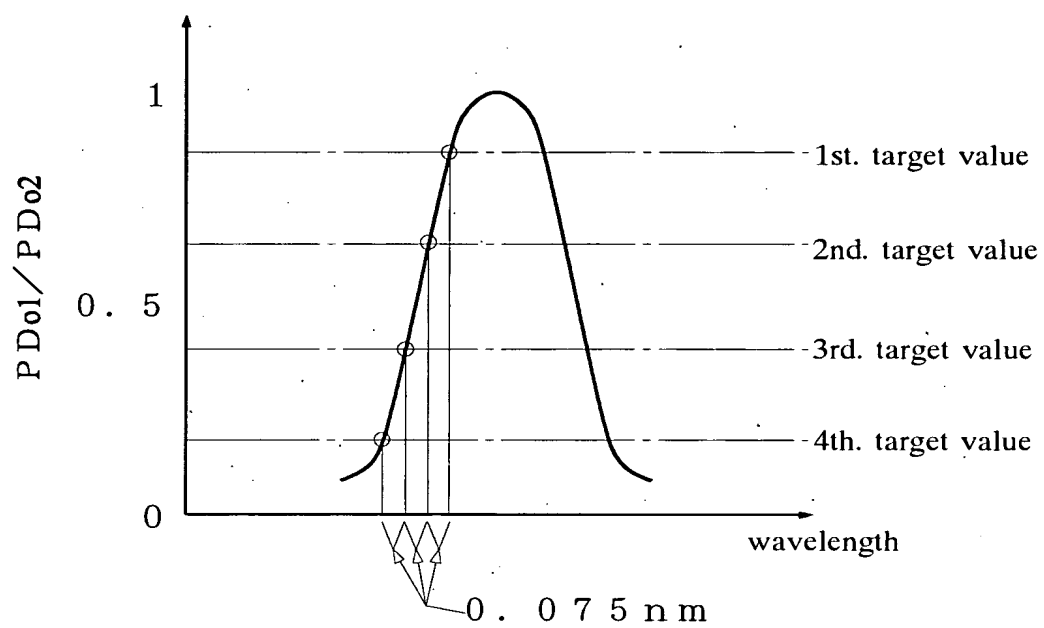
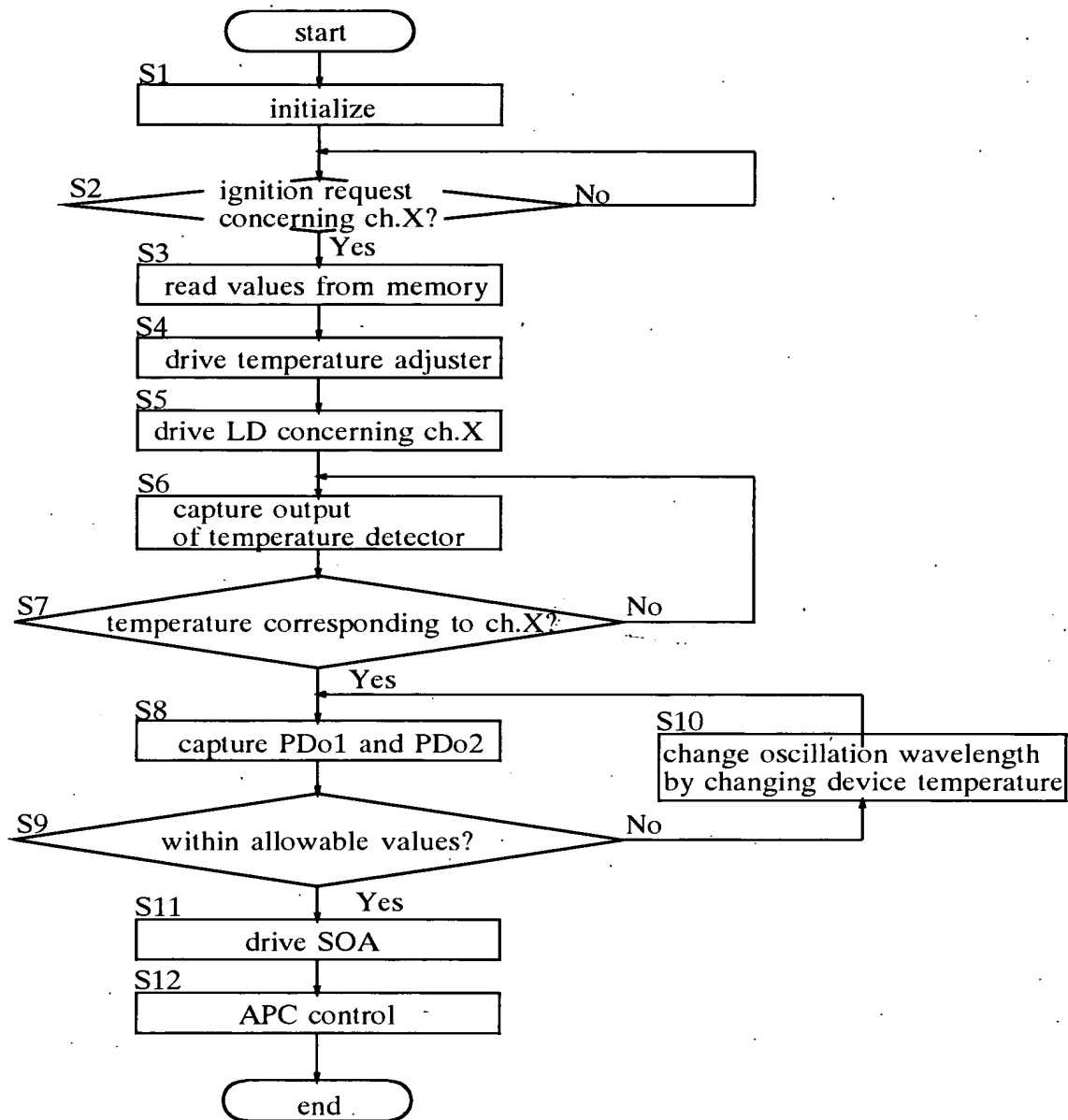


FIG. 6



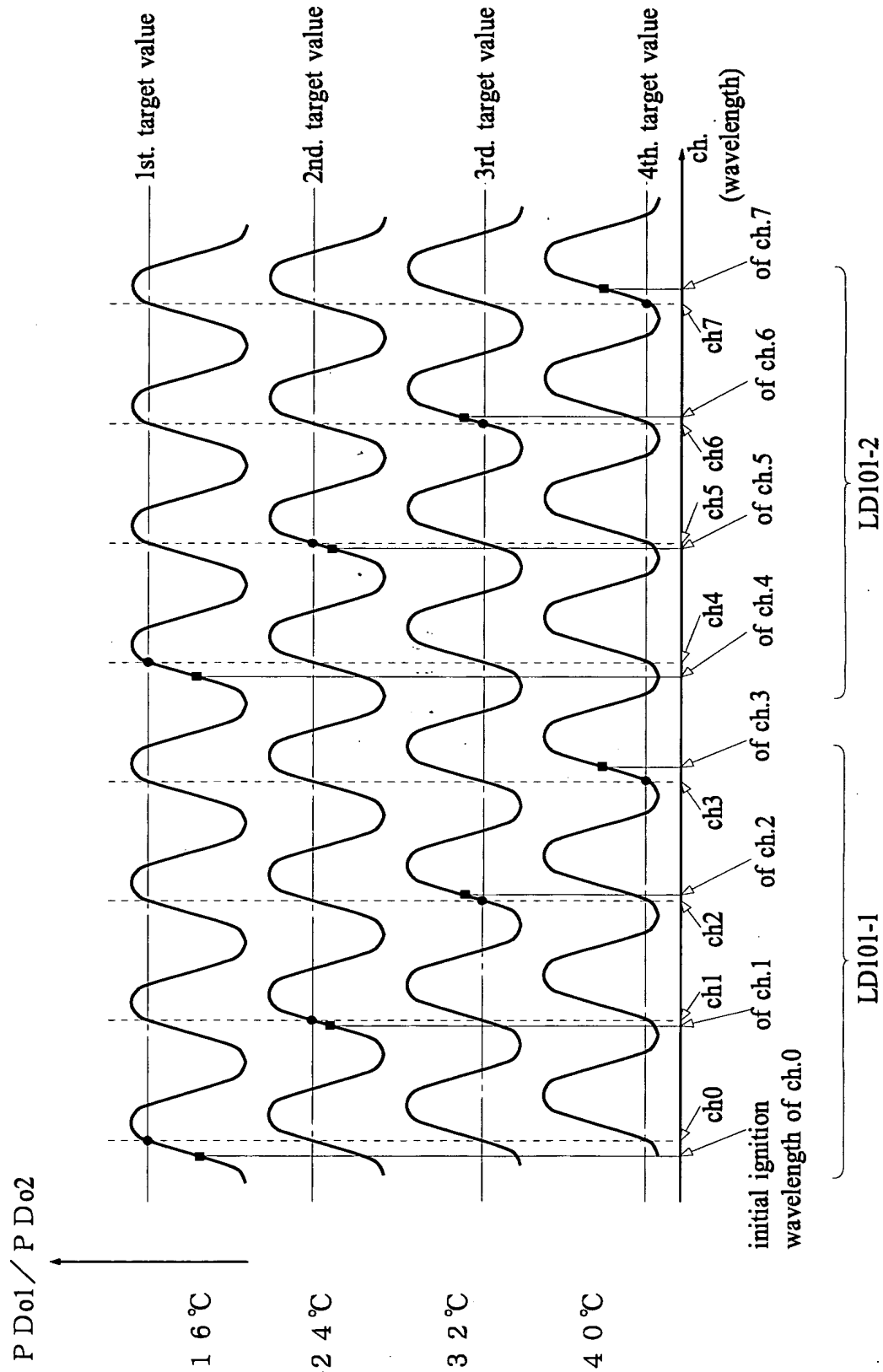


FIG. 8

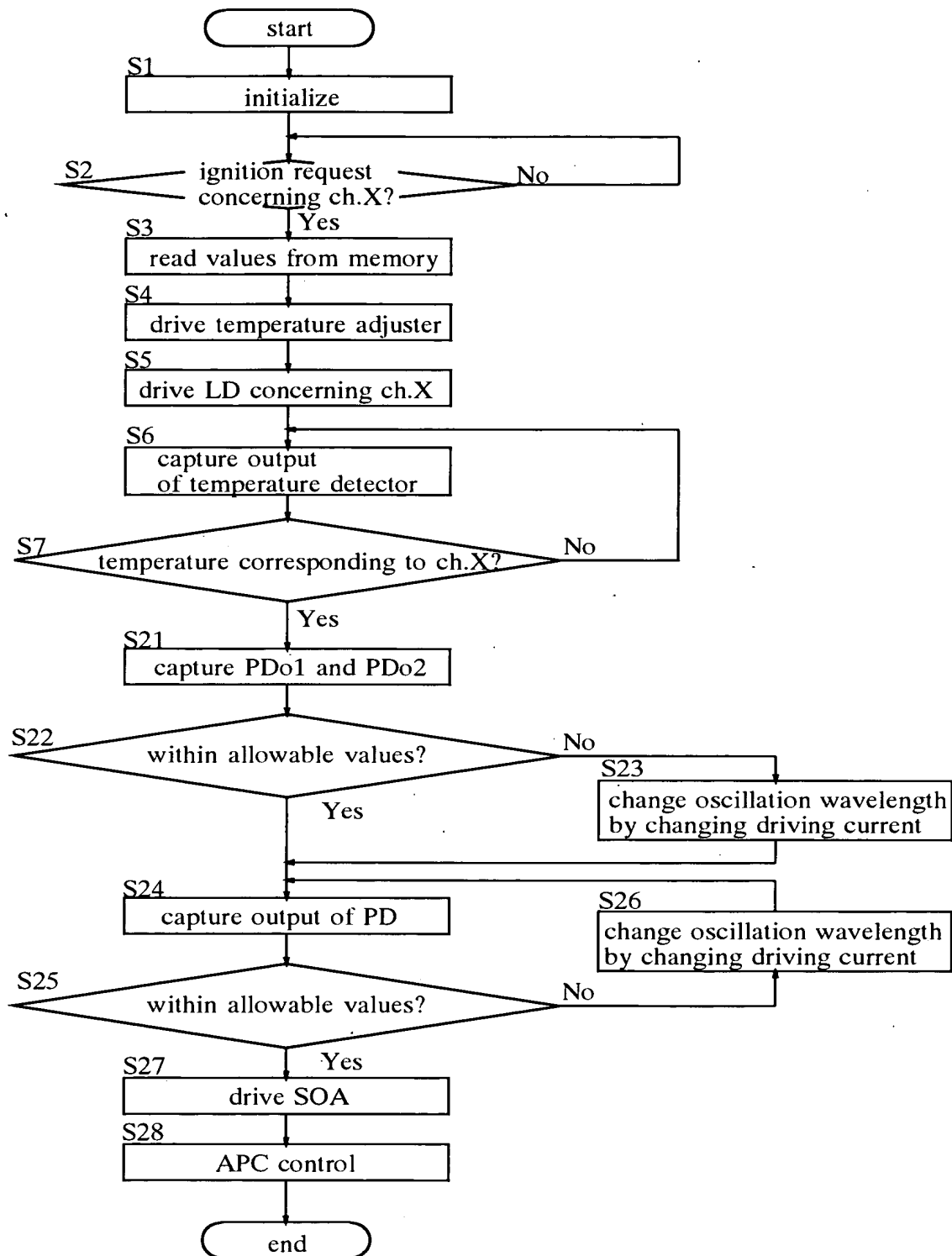


FIG. 9

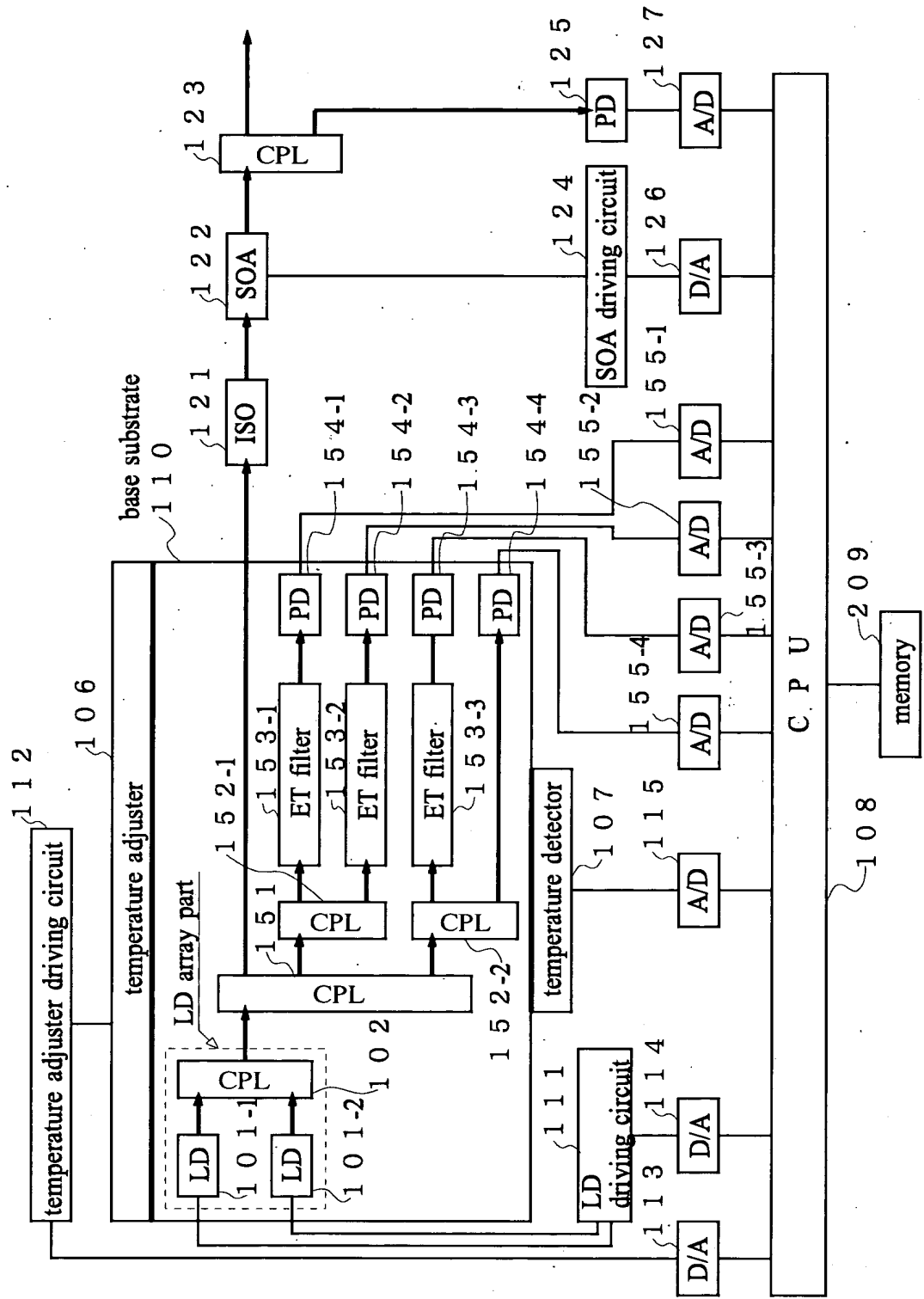


FIG. 10

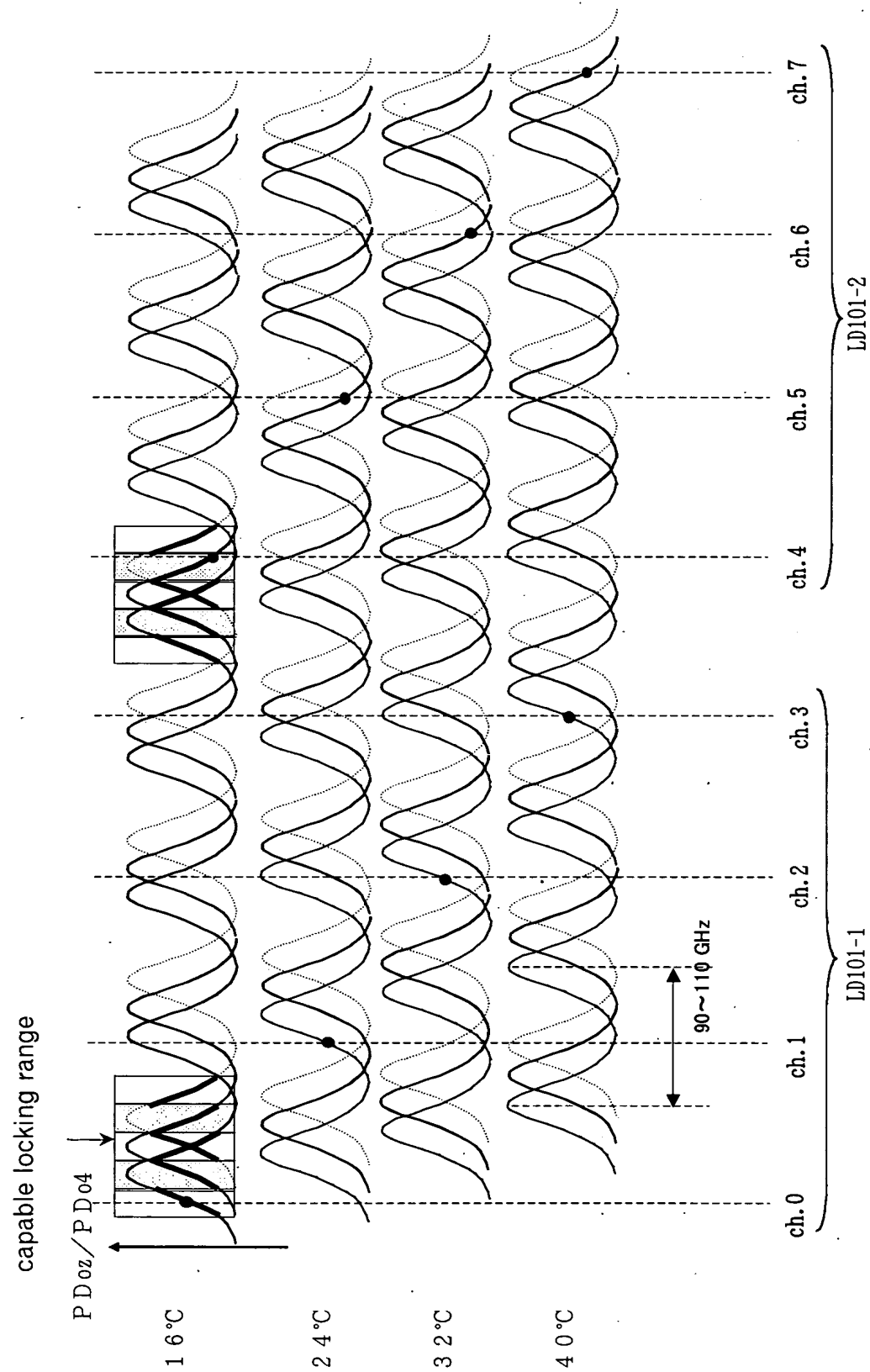
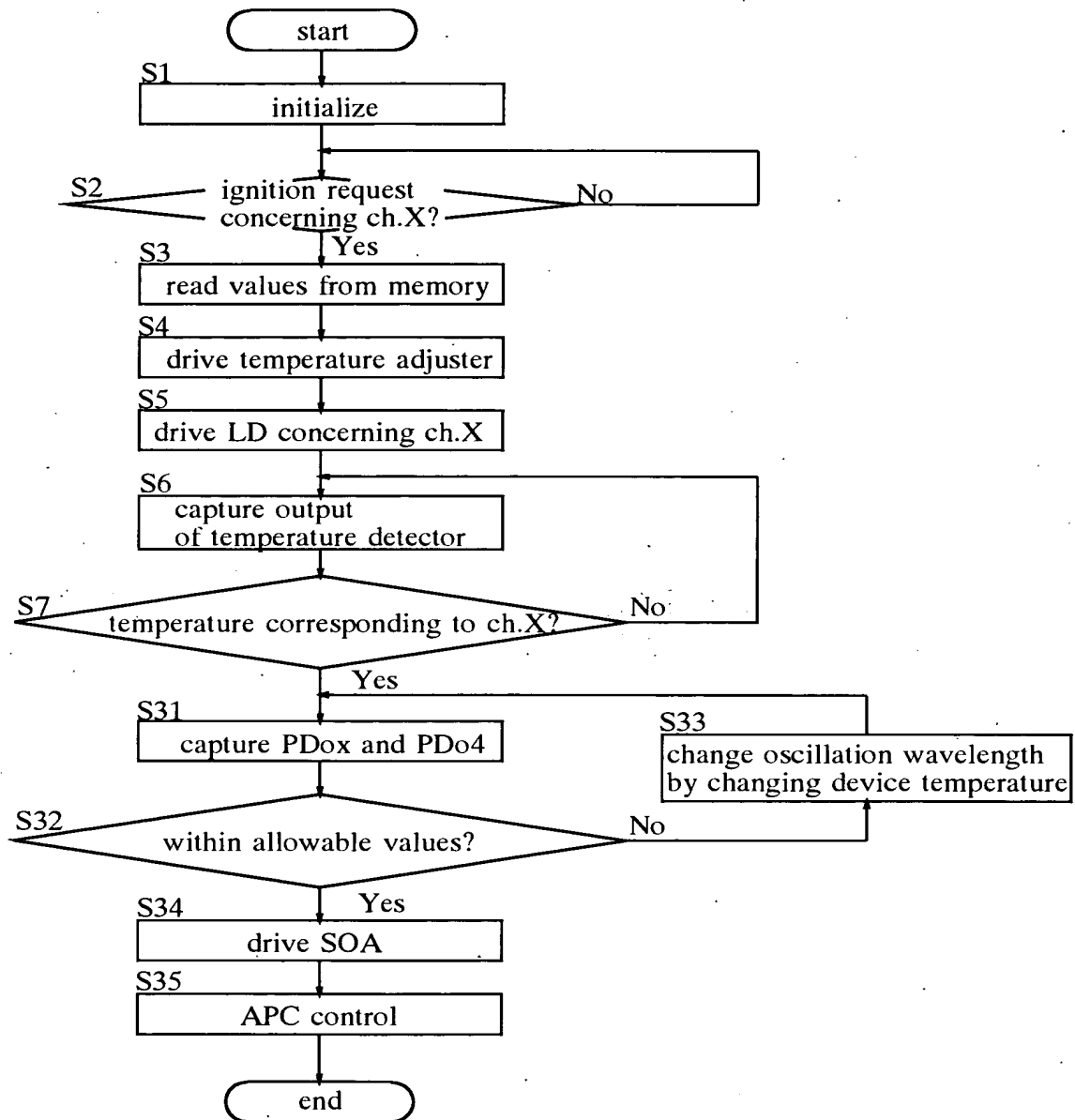


FIG. 11



F I G . 1 2

device temperature (°C)	channel	grid		LD
		(n m)	(T H z)	
1 6	c h 0	1 5 3 5 . 8 2	1 9 5 . 2 0	LD201-1
2 4	c h 1	1 5 3 6 . 6 1	1 9 5 . 1 0	
3 2	c h 2	1 5 3 7 . 4 0	1 9 5 . 0 0	
4 0	c h 4	1 5 3 8 . 1 9	1 9 4 . 9 0	
1 6	c h 5	1 5 3 8 . 9 8	1 9 4 . 8 0	LD201-2
2 4	c h 6	1 5 3 9 . 7 7	1 9 4 . 7 0	
3 2	c h 7	1 5 4 0 . 5 6	1 9 4 . 6 0	
4 0	c h 8	1 5 4 1 . 3 5	1 9 4 . 5 0	
1 6	c h 9	1 5 4 2 . 1 4	1 9 4 . 4 0	LD201-3
2 4	c h 1 0	1 5 4 2 . 9 4	1 9 4 . 3 0	
3 2	c h 1 1	1 5 4 3 . 7 3	1 9 4 . 2 0	
4 0	c h 1 2	1 5 4 4 . 5 3	1 9 4 . 1 0	
1 6	c h 1 3	1 5 4 5 . 3 2	1 9 4 . 0 0	LD201-4
2 4	c h 1 4	1 5 4 6 . 1 2	1 9 3 . 9 0	
3 2	c h 1 5	1 5 4 6 . 9 2	1 9 3 . 8 0	
4 0	c h 1 6	1 5 4 7 . 7 2	1 9 3 . 7 0	
1 6	c h 1 7	1 5 4 8 . 5 1	1 9 3 . 6 0	LD201-5
2 4	c h 1 8	1 5 4 9 . 3 2	1 9 3 . 5 0	
3 2	c h 1 9	1 5 5 0 . 1 2	1 9 3 . 4 0	
4 0	c h 2 0	1 5 5 0 . 9 2	1 9 3 . 3 0	
1 6	c h 2 1	1 5 5 1 . 7 2	1 9 3 . 2 0	LD201-6
2 4	c h 2 2	1 5 5 2 . 5 2	1 9 3 . 1 0	
3 2	c h 2 3	1 5 5 3 . 3 3	1 9 3 . 0 0	
4 0	c h 2 4	1 5 5 4 . 1 3	1 9 2 . 9 0	
1 6	c h 2 5	1 5 5 4 . 9 4	1 9 2 . 8 0	LD201-7
2 4	c h 2 6	1 5 5 5 . 7 5	1 9 2 . 7 0	
3 2	c h 2 7	1 5 5 6 . 5 5	1 9 2 . 6 0	
4 0	c h 2 8	1 5 5 7 . 3 6	1 9 2 . 5 0	
1 6	c h 2 9	1 5 5 8 . 1 7	1 9 2 . 4 0	LD201-8
2 4	c h 3 0	1 5 5 8 . 9 8	1 9 2 . 3 0	
3 2	c h 3 1	1 5 5 9 . 7 9	1 9 2 . 2 0	
4 0	c h 3 2	1 5 6 0 . 6 1	1 9 2 . 1 0	

FIG. 13

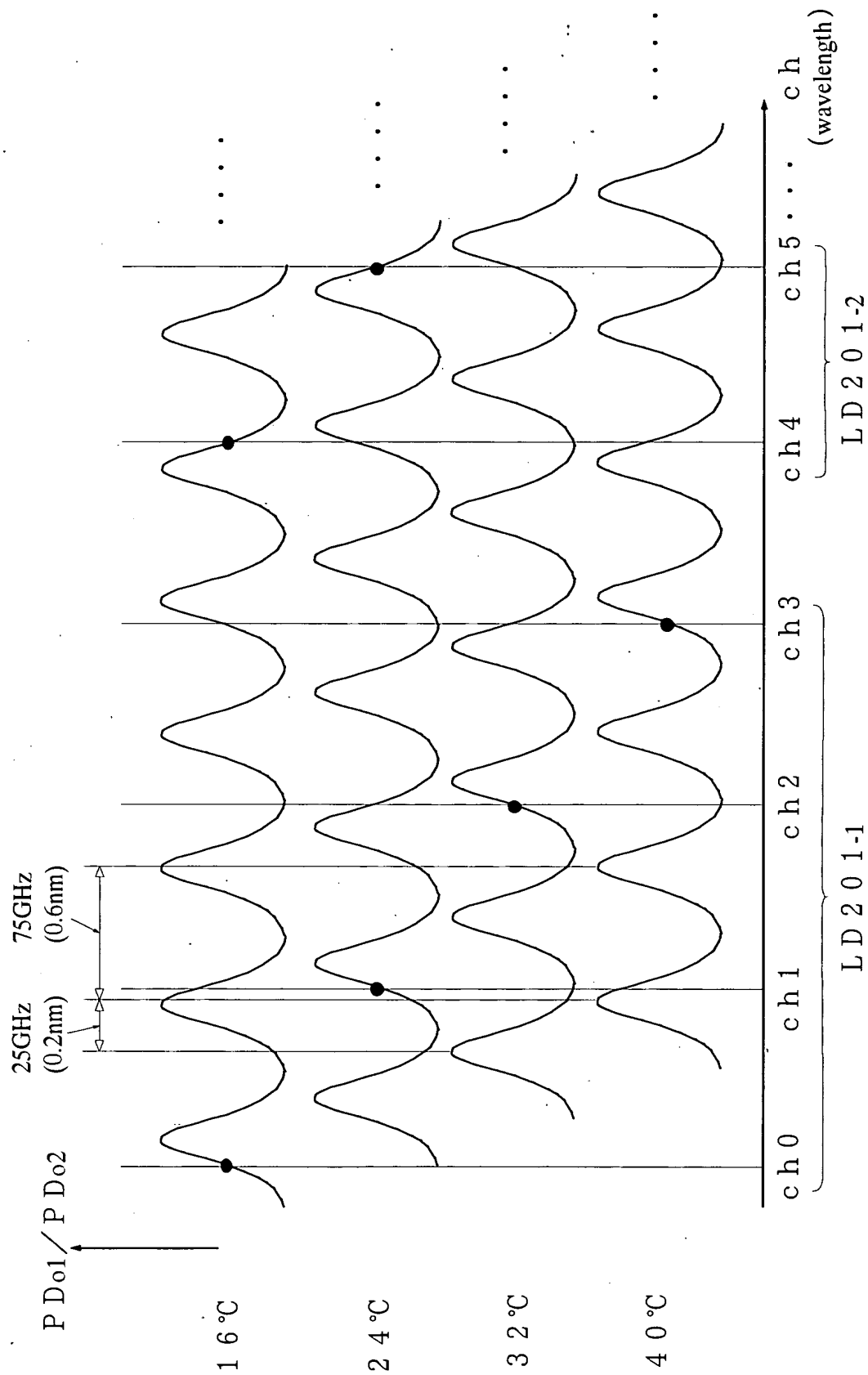


FIG. 14

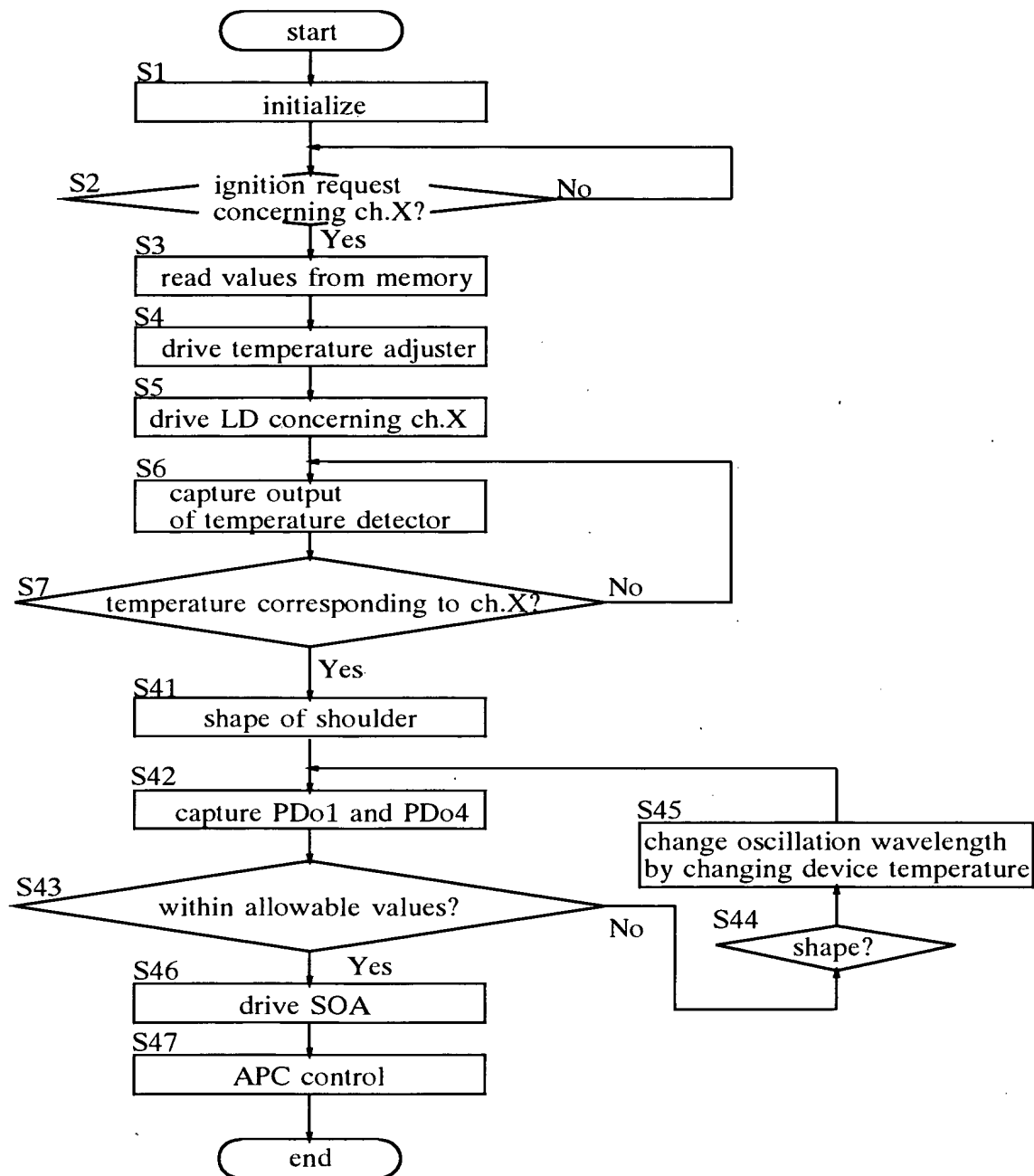


FIG. 15

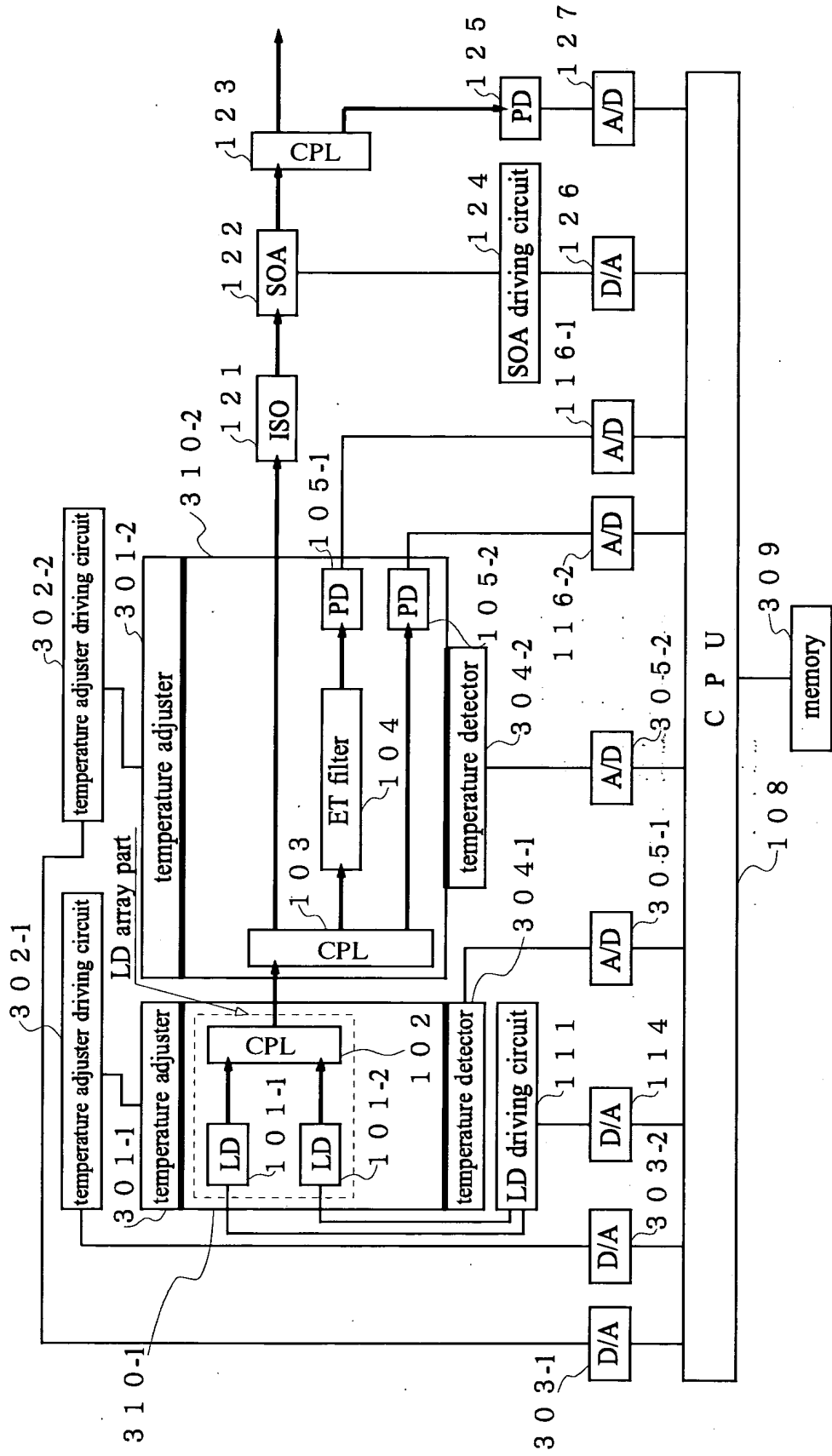


FIG. 16

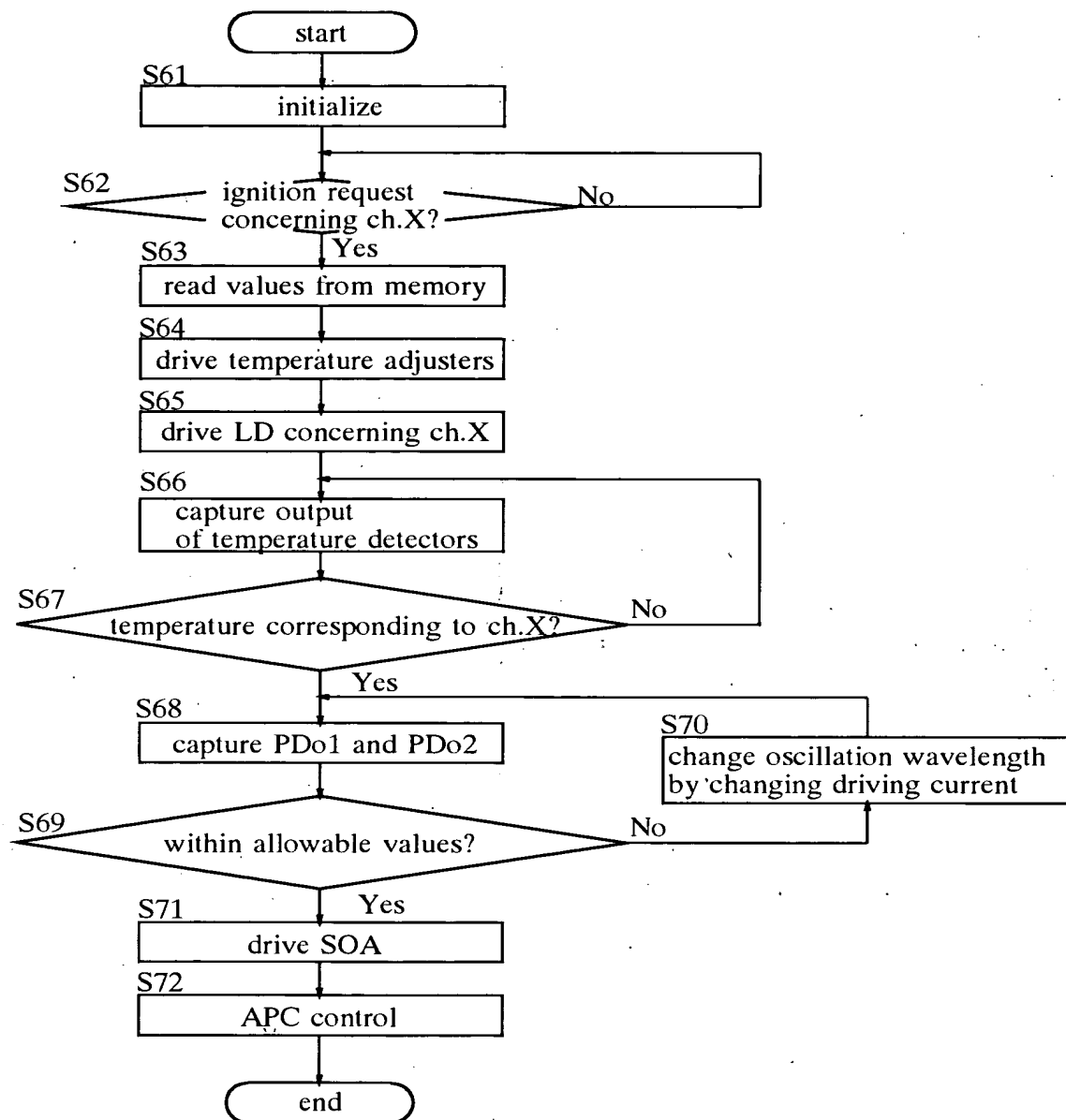


FIG. 17

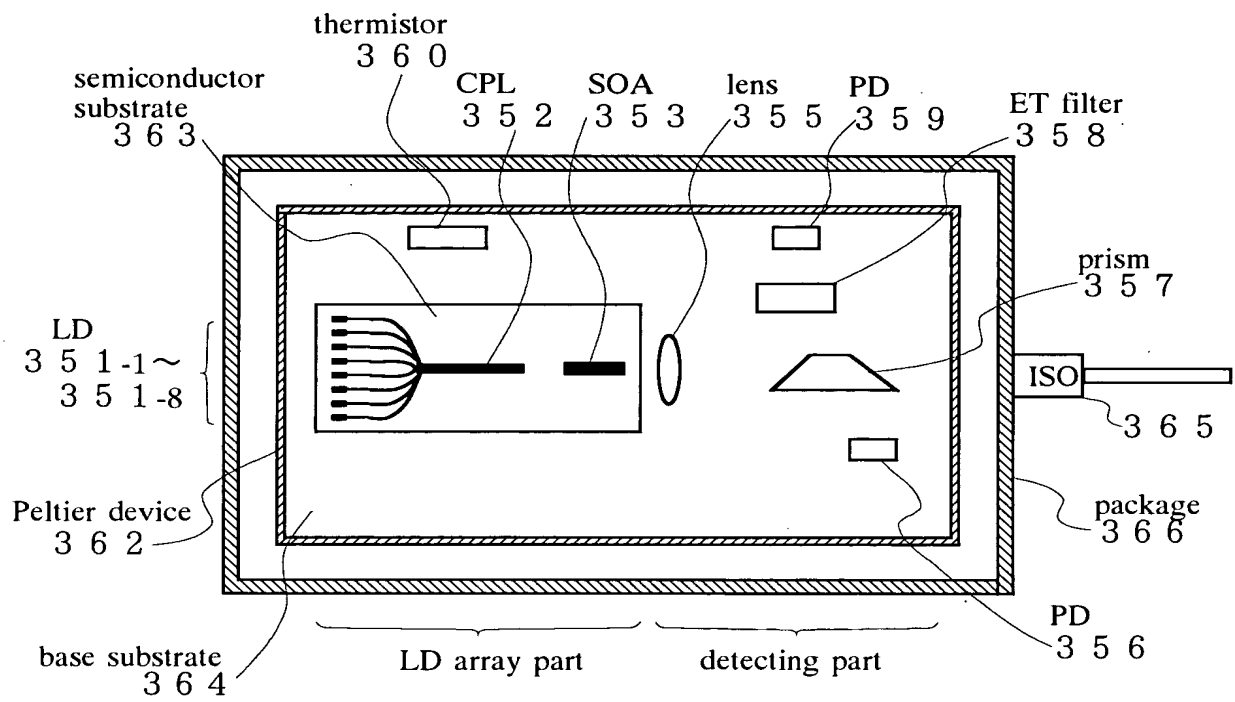
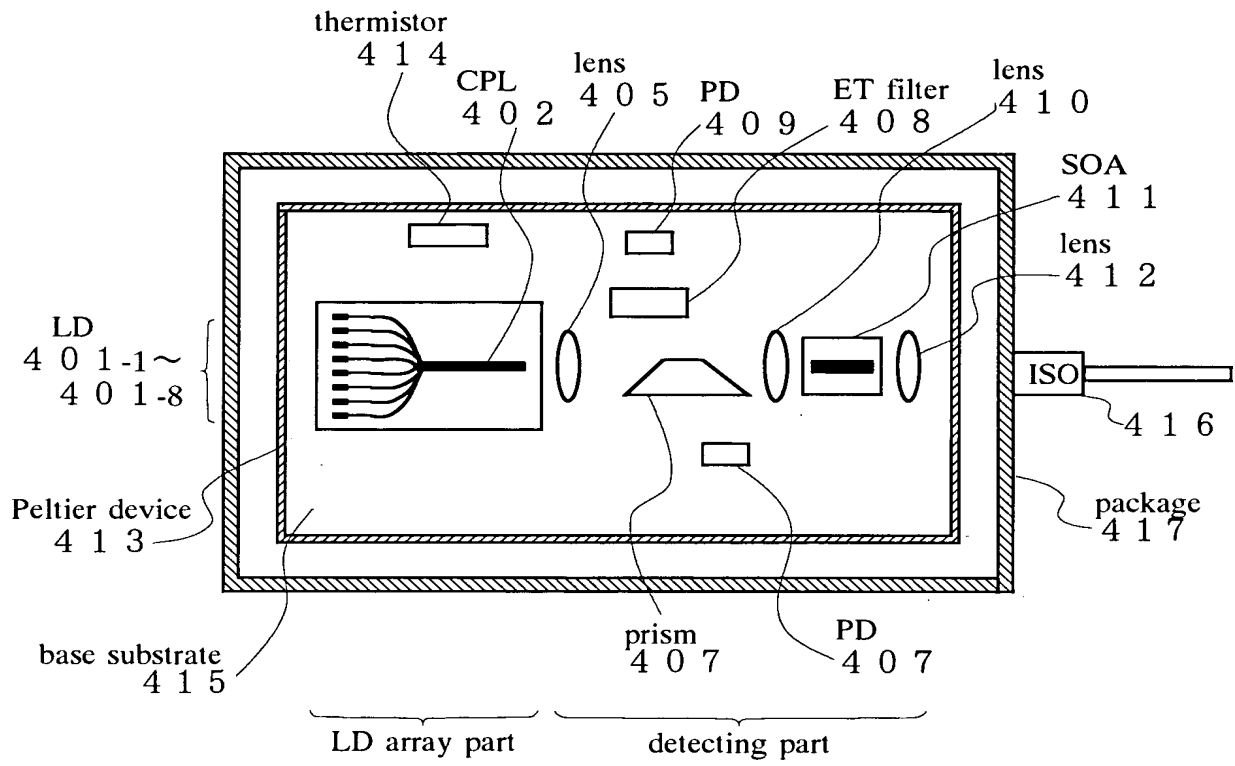
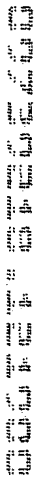


FIG. 18



1



[illegible]

Figure 1 consists of two schematic diagrams, A and B, illustrating the optical system.

Diagram A (Top View): Shows the layout of the system. Key components labeled include:

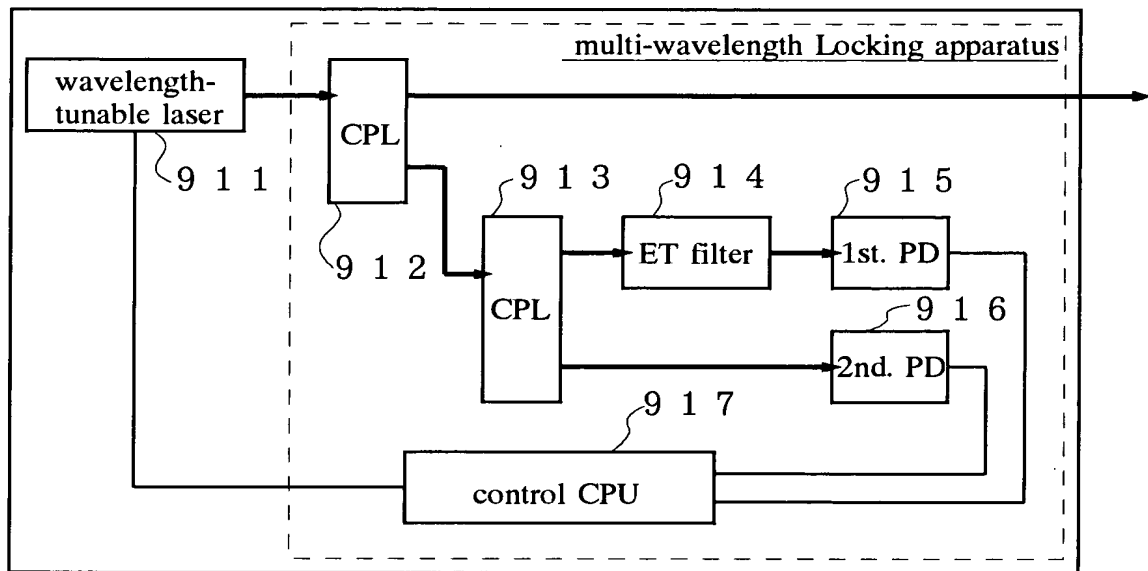
- LD array chip 601:** The central light-emitting component.
- PD 602:** Photodiode on the left side.
- thermistor 609:** Temperature sensor on the left side.
- pins 611-1:** Top connection pins.
- package 600:** The main housing.
- PD 607:** Photodiode on the top right.
- thermistor 608:** Temperature sensor on the top right.
- ET filter 606:** Electrochromic filter on the right.
- prism 605:** Optical prism on the right.
- PD 604:** Photodiode on the bottom right.
- pins 611-2:** Bottom connection pins.
- Section A-A':** Indicated by a line across the middle of the device.

Diagram B (Cross-sectional View): Shows the internal structure along the A-A' line. Key components labeled include:

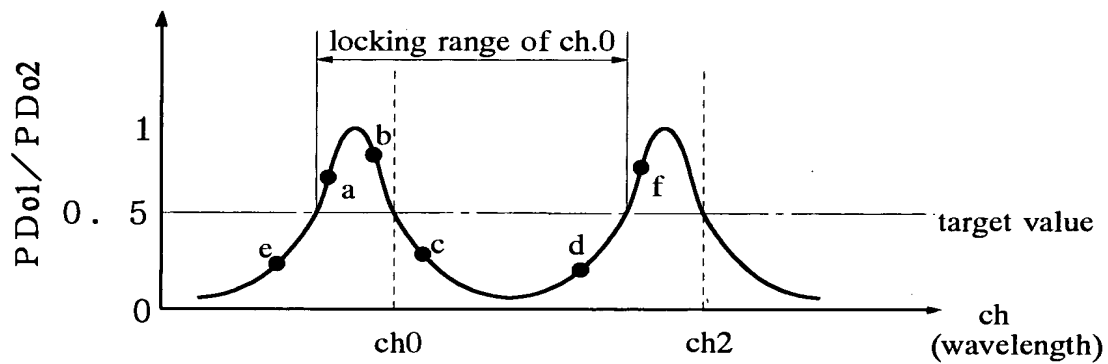
- base substrate 613-1:** The bottom substrate on the left.
- Peltier device 612-1:** Cooling element on the left.
- base substrate 613-2:** The bottom substrate on the right.
- Peltier device 612-2:** Cooling element on the right.
- prism 605:** The optical prism in the center.
- lens 603:** A lens positioned above the prism.
- PD 602:** Photodiode on the left side.
- PD 604:** Photodiode on the right side.
- lens 614:** A lens on the far right.
- package 600:** The main housing.

FIG. 21
Related Art

A.



B.



C.

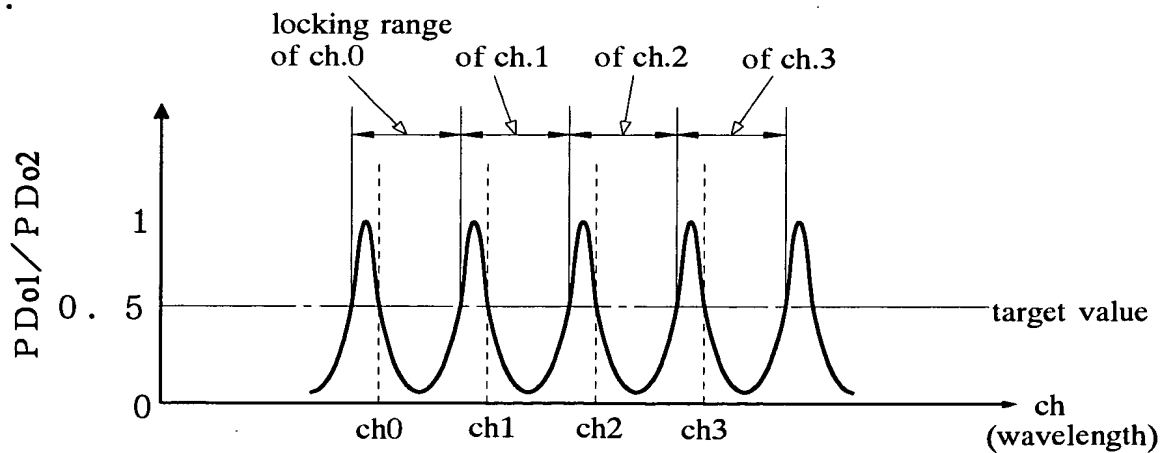


FIG. 22
Related Art

